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is a schematic representation indicating an effect when the apparatus is constructed as shown in Fig. 6 (A).

[0033] Fig. 7(A) and Fig. 7(B) are cross-sectional views of the luminescent elements utilized in the display apparatus shown in Fig. 2, respectively.

5 [0034] Fig. 8(A) and Fig. 8(B) are cross-sectional views of the luminescent elements having a different structure from the pixels shown in Fig. 7(A) and 7(B), respectively.

[0035] Fig. 9 is a graph showing a current-voltage characteristic of the luminescent elements shown in Fig. 7(A) and Fig. 8(B).

10 [0036] Fig. 10 is a graph showing a current-voltage characteristic of the luminescent elements shown in Fig. 7(B) and Fig. 8(A).

[0037] Fig. 11 is a graph showing current-voltage characteristics of an N channel type of TFT.

[0038] Fig. 12 is a graph showing current-voltage characteristics of a P channel type  
15 of TFT.

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[0039] Fig. 13(A)-Fig. 13(B) is a flow sectional view illustrating a method for producing a display apparatus in accordance with the present invention.

[0040] Figs. 14(A) and (B) are respectively a plan view and a cross-sectional view of the pixels having a different structure from the pixels shown in Figs. 3 through 6.

20 [0041] Fig. 15 is an equivalent circuit diagram illustrating a pixel structure of a display apparatus in accordance with Embodiment 1 of the present invention.

[0042] Figs. 16(A) and (B) are a schematic representation indicating electrical connections of each element formed in the pixel shown in Fig. 15, and a waveform chart indicating potential changes in the driving signal, etc., respectively.

25 [0043] Fig. 17 is an equivalent circuit diagram illustrating a structure of a display apparatus in accordance with a modified Embodiment 1 of the present invention.

[0044] Figs. 18(A) and (B) are a schematic representation indicating electrical connections of each element formed in the pixel shown in Fig. 17, and a waveform chart indicating potential changes in the driving signal, etc. respectively.

30 [0045] Fig. 19 is an equivalent circuit diagram illustrating a pixel structure of a display apparatus in accordance with Embodiment 2 of the present invention.